

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES**

In re Application of: : Osamu SUZUKI et al Group Art Unit: 2811

Application No. 10/722,597 : Examiner: Monica Lewis

Filed: 11/28/2003 : Confirm. No: 8275

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND SEMICONDUCTOR
INTEGRATED CIRCUIT CHIP THEREOF

**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER
TO THE BOARD OF APPEALS**

Mail Stop: APPEALS

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

September 4, 2007

Sir:

In accordance with the provisions of 37 C.F.R. 41.31, Applicants hereby appeal from the rejection of claims 4-14 and 21, as set forth in the Final Rejection mailed April 2, 2007.

A Petition for a two-month extension of time, and the requisite fee (\$450), is included herewith.

Also included is the Amex credit card authorization, authorizing the additional amount of \$500 to be paid for the Appeal Fee .

Respectfully Submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

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